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update

EAST History

7/11/2005

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|-----|--------|--|--|-----------|
| BRS | 105886 | transceivers | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 58 | S39 and (programmable adj circuitry) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 66 | S39 and (programmable adj logic adj circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 122 | S40 or S41 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | S42 and (auto-negotiation) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 95 | S42 and memory | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 2 | S42 and (negotiation or negotiating) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | S42 and (transceiver adj attributes) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | transceiver adj attributes | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 2612 | session adj information | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | S42 and S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 243 | S39 and S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 122 | S39 and S42 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | S51 and S48 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |
| BRS | 0 | S39 and (transceiver adj attributes) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | 11-Jul-05 |